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Created on Tue Apr 17 22:21:29 2012

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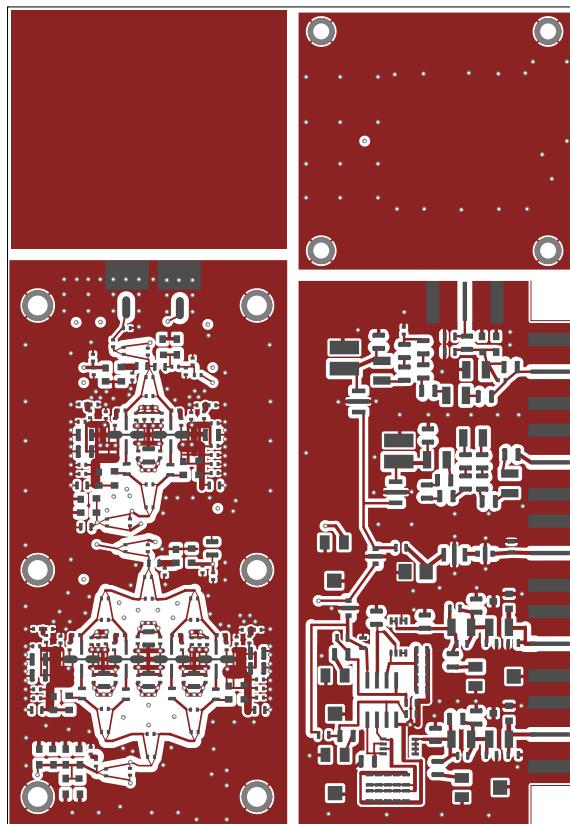
14. toppaste

15. bottompaste

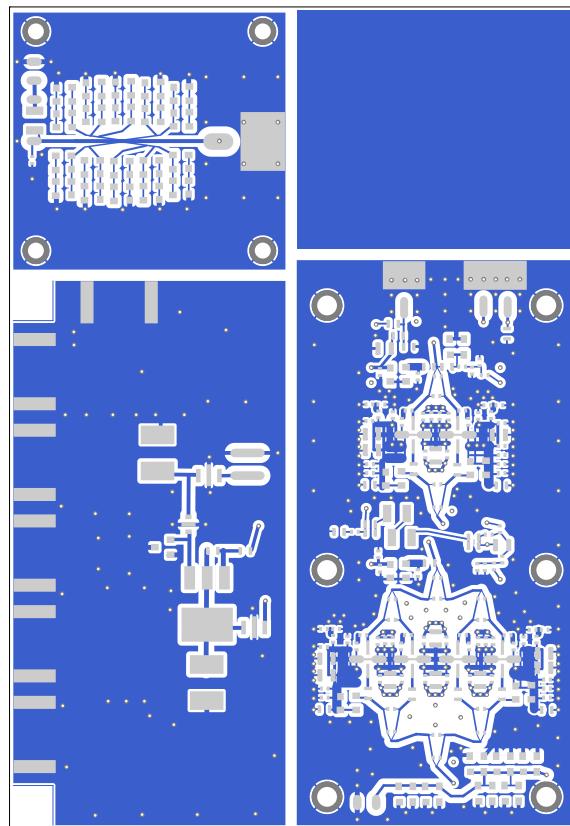
16. topassembly

LNA4R/lna4r3.pcb
LNAT4R/bottomassembly

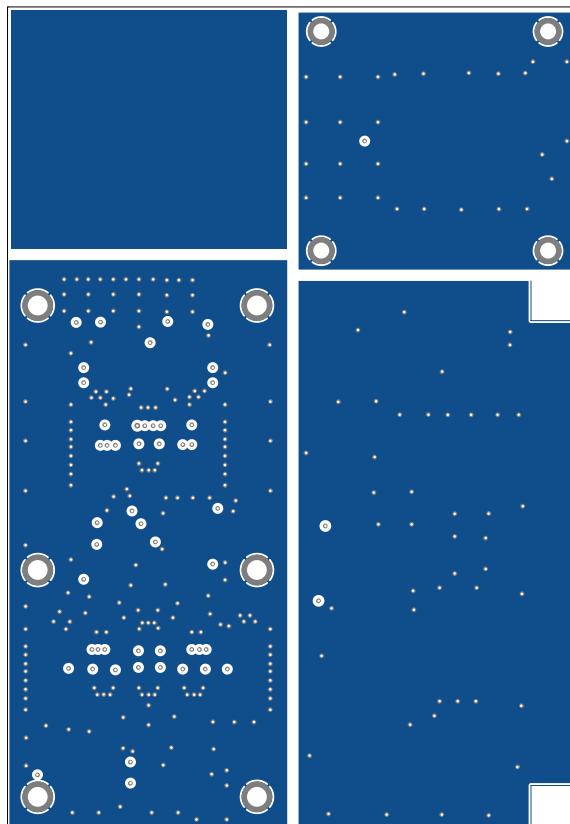
18. f1



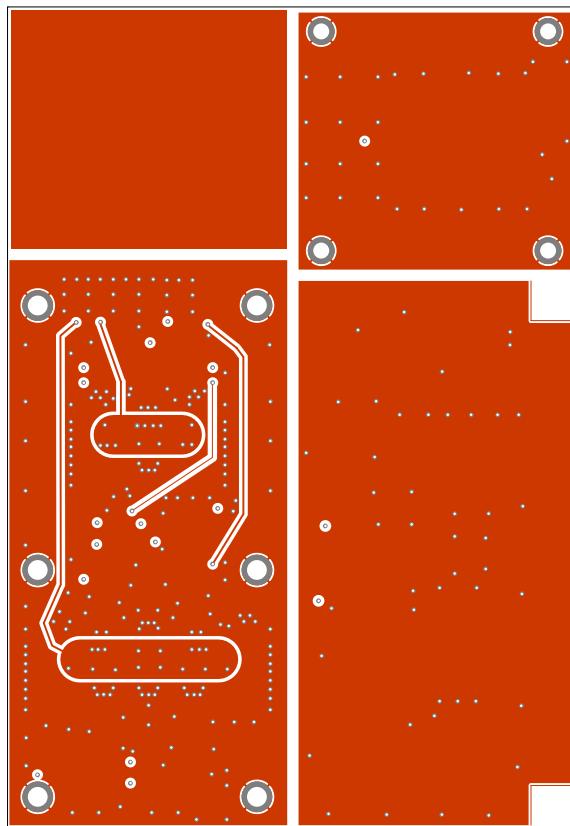
top, scale = 1:1.000
LNA4R/lna4r3.pcb



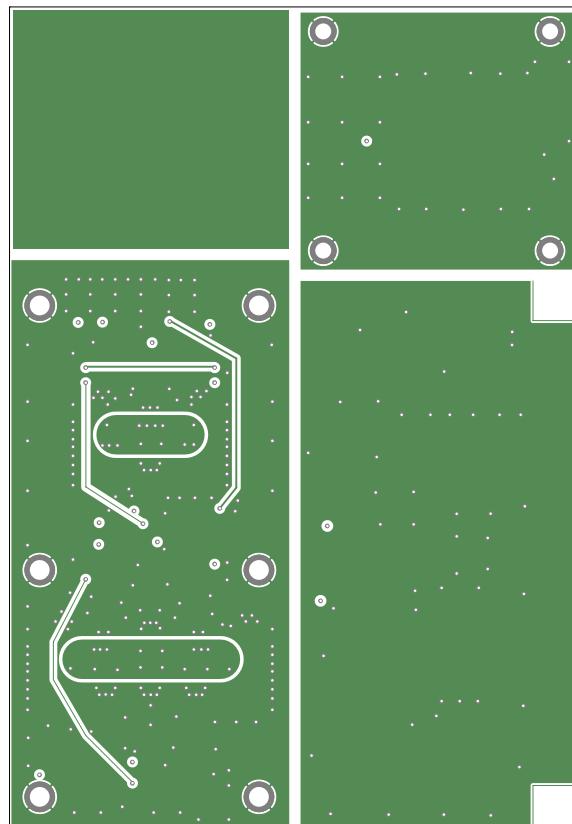
bottom (mirrored), scale = 1:1.000
LNA4R/lna4r3.pcb



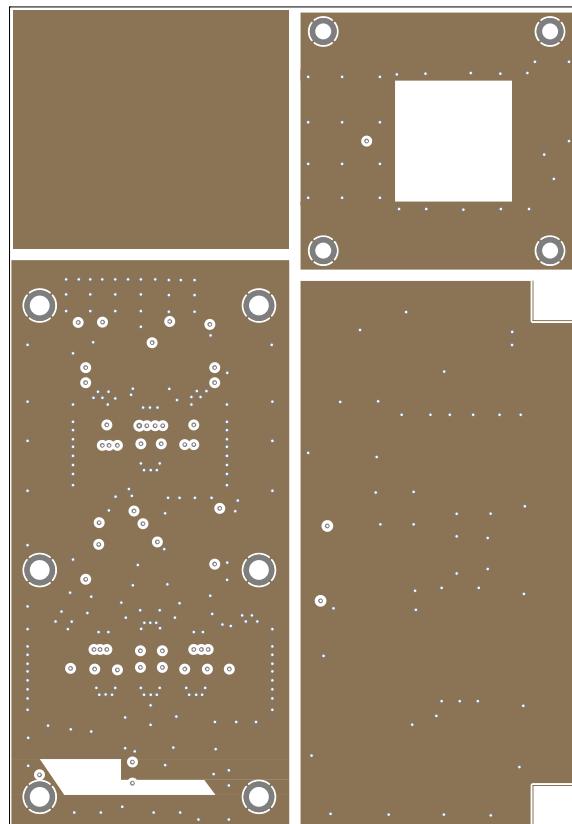
group2, scale = 1:1.000
LNA4R/lna4r3.pcb



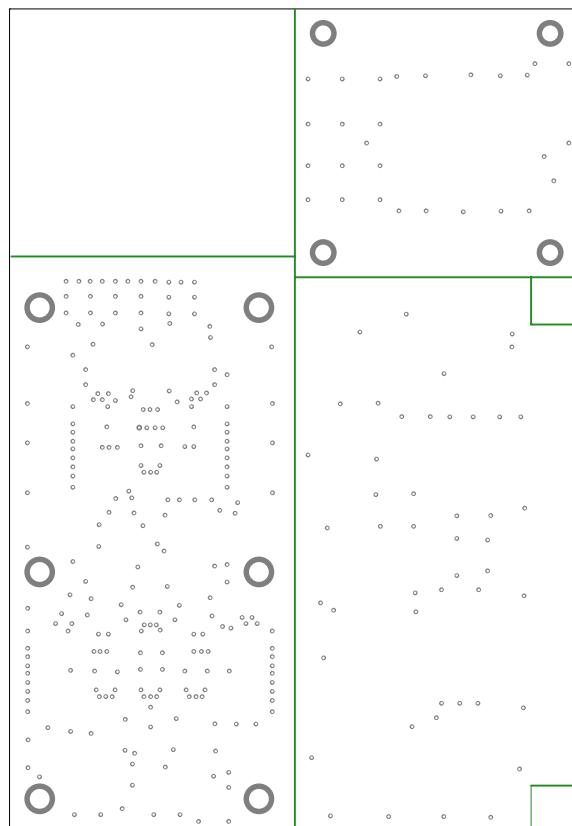
group3, scale = 1:1.000
LNA4R/lna4r3.pcb



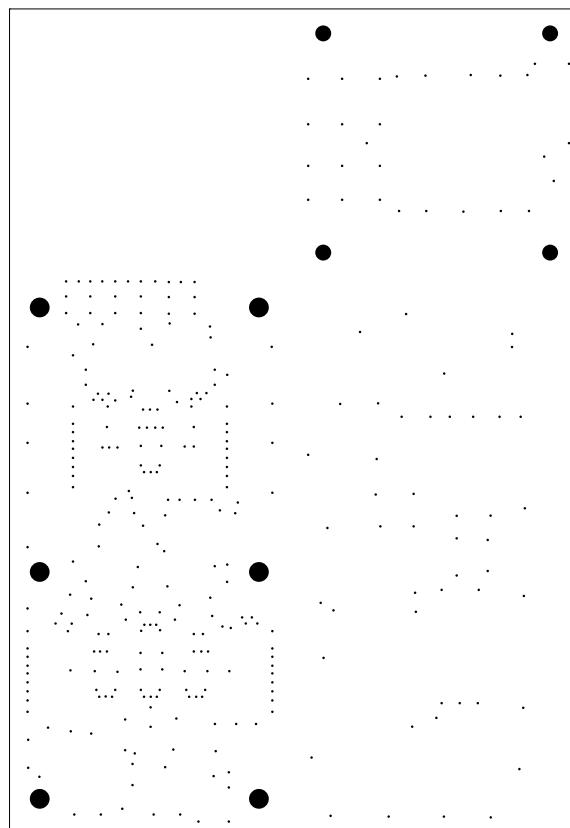
group4, scale = 1:1.000
LNA4R/lna4r3.pcb



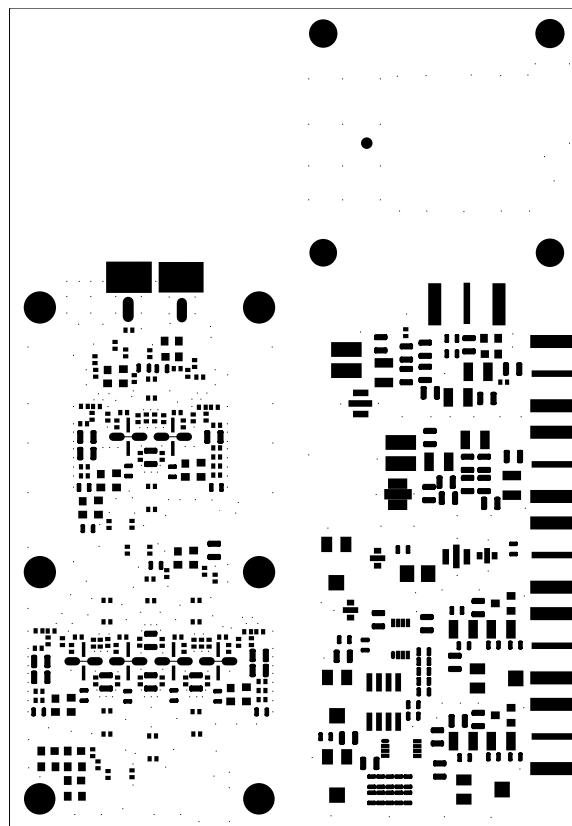
group5, scale = 1:1.000
LNA4R/lna4r3.pcb



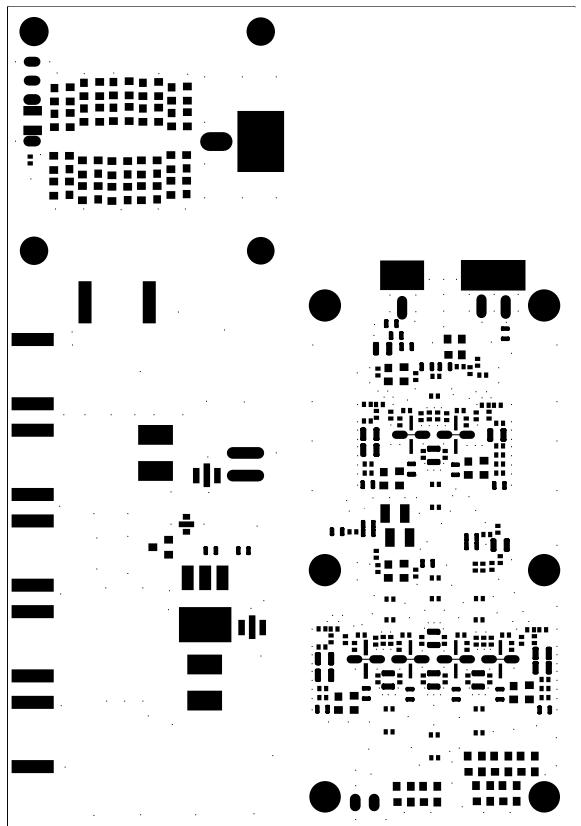
group7, scale = 1:1.000
LNA4R/lna4r3.pcb



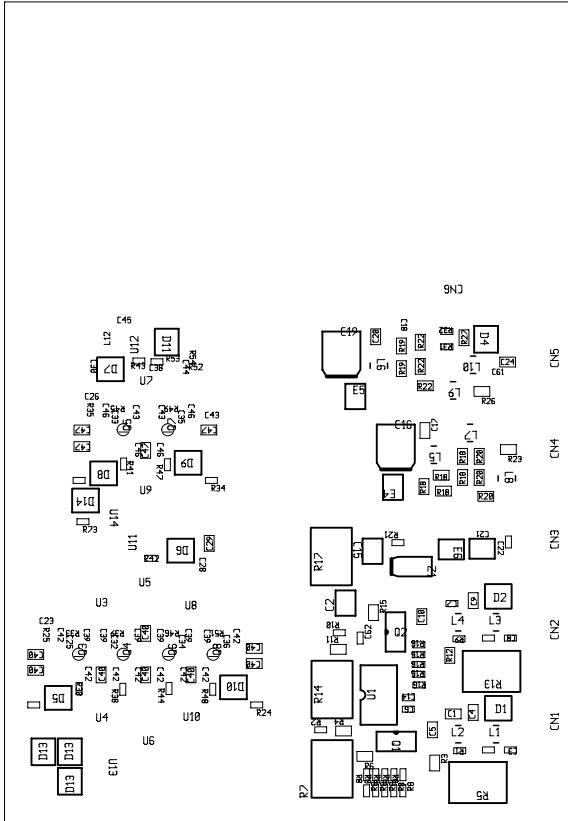
plated-drill, scale = 1:1.000
LNA4R/lna4r3.pcb



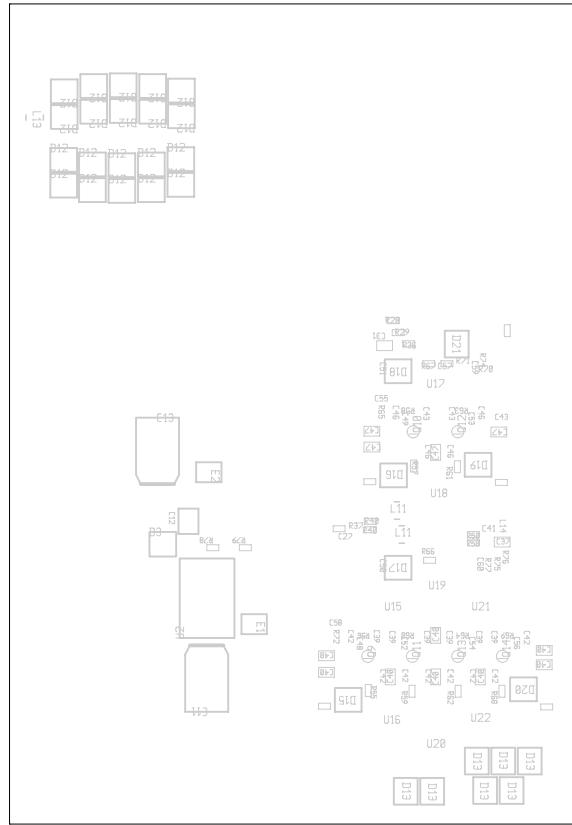
topmask, scale = 1:1.000
LNA4R/lna4r3.pcb



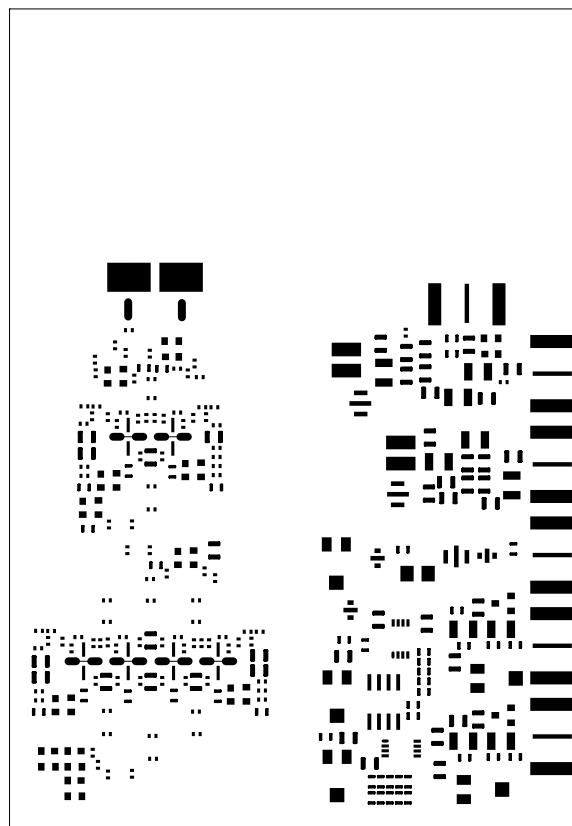
bottommask (mirrored), scale = 1:1.000
LNA4R/lna4r3.pcb



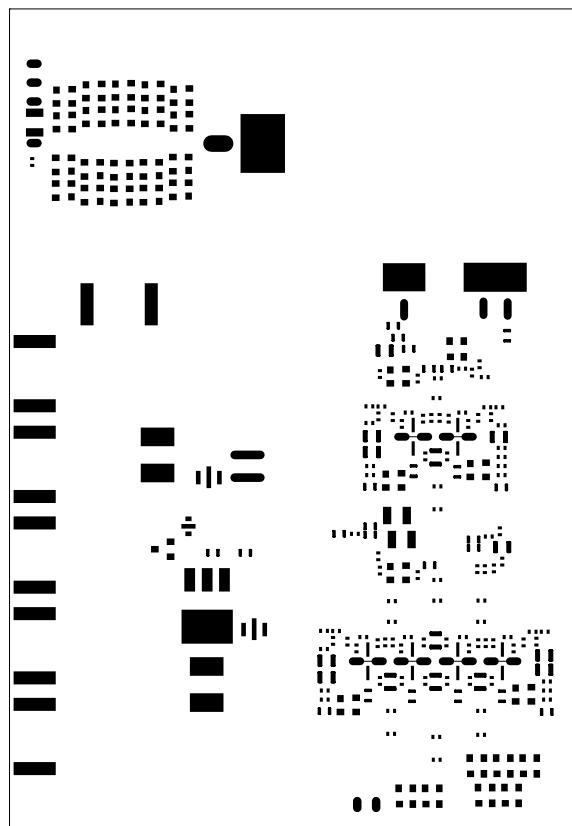
topsilk, scale = 1:1.000
LNA4R/Lna4r3.pcb



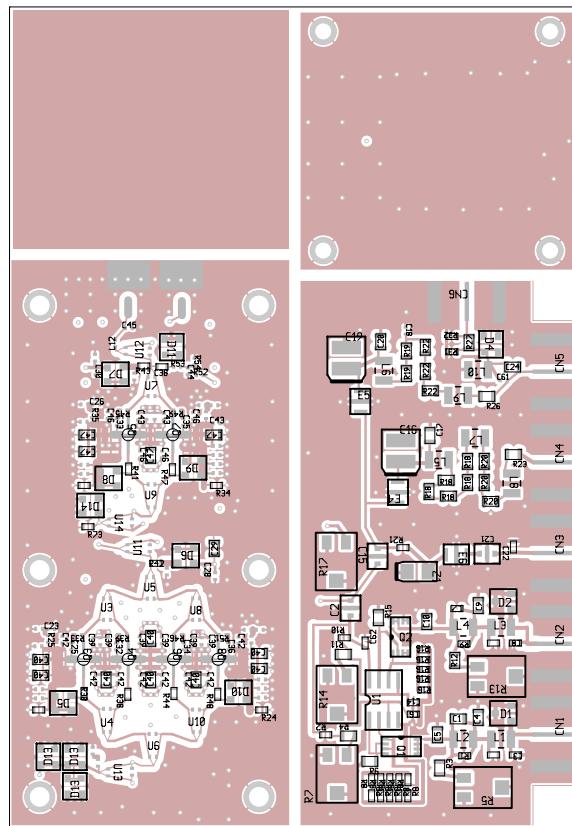
bottomsilk (mirrored), scale = 1:1.000
LNA4R/lna4r3.pcb



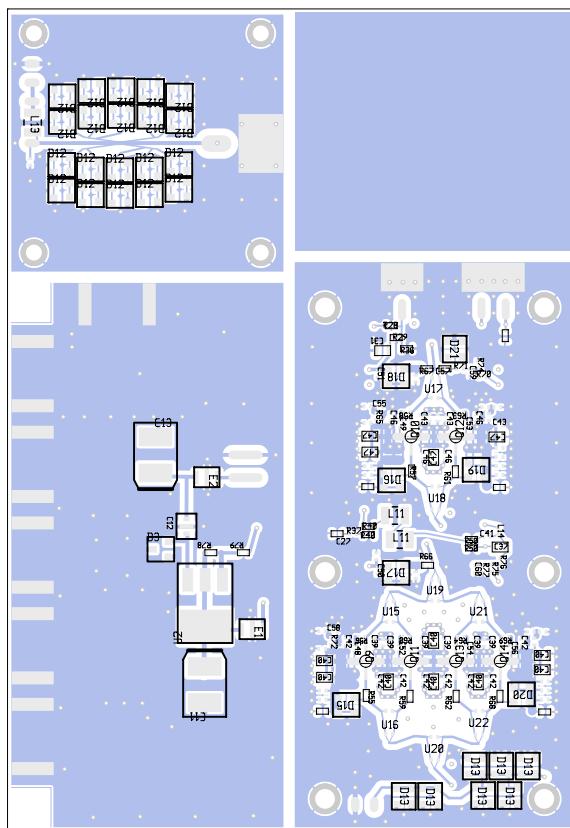
toppaste, scale = 1:1.000
LNA4R/lna4r3.pcb



bottompaste (mirrored), scale = 1:1.000
LNA4R/lna4r3.pcb



topassembly, scale = 1:1.000
LNA4R/lna4r3.pcb



bottomassembly (mirrored), scale = 1:1.000
LNA4R/lna4r3.pcb

There are 3 different drill sizes used in this layout, 321 holes total

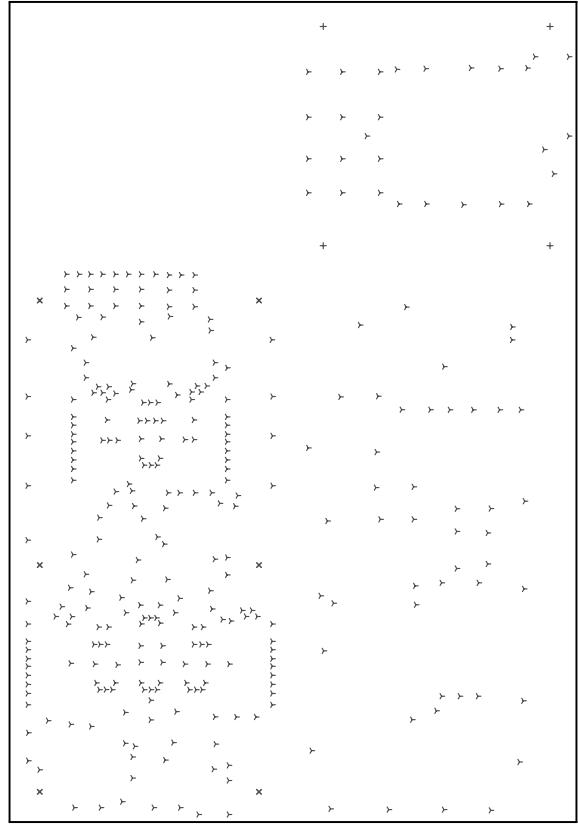
Symbol	Diam. [Inch]	Count	Plated?
x	0.013	311	YES
+	0.069	4	YES
x	0.103	6	YES

Title: [Unknown] - Fabrication Drawing

Author: morthw

Date: ■■■ 4/17 13:21:30 2012 UTC

Maximum Dimensions: 4271.650000 mils wide, 2952.760000 mils high



Board outline is the centerline of this 8.000000 mil rectangle - 0,0 to 4271.650000,2952.760000 mils